## ABSTRACT OF THE DISCLOSURE

A memory system according to the present invention copies data stored in memory modules to a hard disk device at each predetermined period, in replacing an arbitrary memory module, switches a bus from a unidirectional bus to a bi-directional bus, and at the time when an access to a memory module to be replaced is requested, accesses a storage area in the hard disk corresponding to an address space of the memory module. In addition, the memory system copies data corresponding to the address space of the memory module to be replaced from the hard disk device to a storage, and at the time when an access to the memory module is requested, accesses a storage area of the storage corresponding to the address space. Moreover, the memory system short-circuits bus connection which is disconnected by removing the memory module to be replaced.

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